

**FINNEGAN**FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, LLP  
WWW.FINNEGAN.COM**FACSIMILE TRANSMITTAL****Subject:** Priority and Information Disclosure  
Statement**Date:** January 13, 2009**Client/Matter No.:** 04329. [REDACTED] 2350**Submitted by:** Kathy Martin**Your Reference:** 09/617,138**MD:** 610A**TO****FROM****Name:** John Ingram**Name:** Richard V. Burgujian**Firm:** U.S. Patent and Trademark Office**Phone No.:** 571-203-2790**Fax No.:** 571-273-8793**Approved:****Phone No.:** 571-272-8793**# Pages (Incl. this):** 7**Confirmation Copy to Follow: Yes: X No:****Message:**

Good afternoon, John.

In accordance with our telephone conversation earlier today, please find enclosed copies of the following:

Copy of date-stamped post card dated 7/14/00, reflecting an IDS filed.

Copy of IDS filed 7/14/00 - **Please note, statement of relevance is provided.**

Copy of date-stamped post card dated 11/6/00, reflecting priority document filed.

Copy of front page of the original priority document.

If you need any additional information, please do not hesitate to contact me.  
Thank you again for your assistance in this matter.Katherine L. Martin  
U.S. Filing and Prosecution Specialist**If there is a problem with this transmission, notify the sender at the number above.****This facsimile is intended only for the individual to whom it is addressed and may contain information that is privileged, confidential, or exempt from disclosure under applicable law. If you have received this facsimile in error, please notify the sender immediately by telephone (collect), and return the original message by first-class mail to the address below.**TWO FREEDOM SQUARE | 11955 FREEDOM DRIVE | RESTON, VA 20190-5675  
PHONE: 571.203.2700 | FAX: 202.408.4400

RVB  
RECEIVED  
CENTRAL FAX CENTER  
JAN 13 2009

PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:  
New U.S. Application for: SEMICONDUCTOR DEVICE AND ITS  
MANUFACTURING METHOD

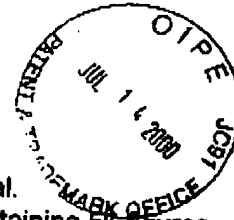
Inventor: Osamu ARISUMI

**BOX PATENT APPLICATION**

1. Check for \$768.00
2. Transmittal Letter
3. Spec. 51 pgs. 4 indep. clms. and 20 clms. total.
4. Drawings - 23 sheets of formal drawings containing 58 figures
5. Information Disclosure Statement and Information Disclosure Citation,  
PTO 1449 with document attached.

Dated: July 14, 2000  
CASE REF: 04329.2350  
RVB/FPD/sci

JUL 14 2000  
(Due Date: July 14, 2000)



RVB  
7/17/00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
 )  
 Osamu ARISUMI )  
 )  
 Serial No.: Not Yet Assigned ) Group Art Unit: Not Yet Assigned  
 )  
 Filed: July 14, 2000 ) Examiner: Not Yet Assigned

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

Assistant Commissioner for Patents  
 Washington, DC 20231

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicant brings to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicant respectfully requests that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is listed on the accompanying PTO-1449 and is in a non-English language:

Japanese Patent Application No. 11-204746, discloses a PZT ferroelectric capacitor is fabricated by a seeding method.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document

LAW OFFICES  
 FINNEGAN, HENDERSON,  
 ARABOW, GARRETT  
 & DUNNER, L.L.P.  
 1300 K STREET, N.W.  
 WASHINGTON, D.C. 20005  
 202-408-4000

Attorney Docket No.: 04329.2350

is material or constitutes "prior art." If the Examiner applies the listed document as prior art against any claim in the application and applicant determines that the cited document does not constitute "prior art" under United States law, applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such document.

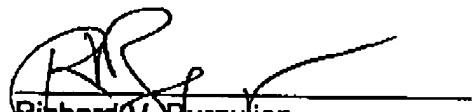
Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should it be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By:

  
Richard V. Burgulian  
Reg. No. 31,744

Dated: July 14, 2000

Enclosures  
RVB/FPD/sci

LAW OFFICES  
FINNEGAN, HENDERSON,  
FARABOW, GARRETT  
& DUNNER, L.L.P.  
300 I STREET, N.W.  
WASHINGTON, D. C. 20005  
202-400-4000

**RECEIVED**  
**CENTRAL FAX CENTER**  
**JAN 13 2009**

OMB No. 0651-0011

**INFORMATION DISCLOSURE CITATION**  
 (Use several sheets if necessary)

Atty. Docket No. 04329.2350				Serial No. Not Yet Assigned			
Applicant Osamu ARISUMI							
Filing Date July 14, 2000				Group Not Yet Assigned			
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial*		Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Sub Class	Translation Yes or No
		11-204746	07/30/99	Japan			No
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
Examiner				Date Considered			
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							
Form PTO 1449				Patent and Trademark Office - U.S. Department of Commerce			

PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:

In Re Application of : Osamu ARISUMI

Serial No.: 09/617,138. Group Art Unit: 2811

Filed: July 14, 2000

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

**BOX MISSING PART**

1. Response To Notice To File Missing Parts
2. Copy of Notice To File Missing Parts
3. Declaration and Power of Attorney
4. Check for \$130.00 for surcharge fee
5. Claim for Priority
6. Certified Copy of Japanese Patent Application No. 11-200901, filed on July 14, 1999



Dated: November 6, 2000

CASE REF: 04329.2350

(Due Date: November 6, 2000)

RVB: [redacted] soci

NOV 06 2000

*11/05/00*  
*R*

日本国特許庁  
PATENT OFFICE  
JAPANESE GOVERNMENT

別紙添付の書類に記載されている事項は下記の出願書類に記載されて  
いる事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed  
with this Office.

出願年月日  
Date of Application:

1999年 7月14日

出願番号  
Application Number:

平成11年特許願第200901号

出願人  
Applicant(s):

株式会社東芝

2000年 7月21日

特許庁長官  
Commissioner,  
Patent Office

及川耕造

